High-Assurance SPIRAL

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#kievfprog, September 2017

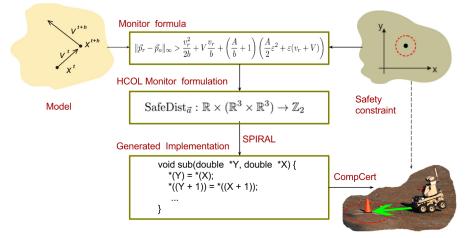
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"Formal Methods refers to mathematically rigorous techniques and tools for the specification, design and verification of software and hardware systems"

http://shemesh.larc.nasa.gov/fm/fm-what.html

A Motivating Example



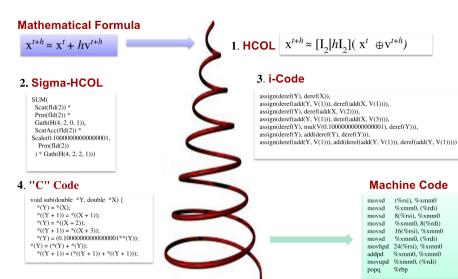
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Under the hood - SPIRAL



Scope and Status

- Physical meaning out of scope
- HCOL formalization done
- HCOL correctness proofs done
- Σ-HCOL formalization done
- Σ-HCOL correctness proofs work in progress
- *i-Code* correctness proofs *future work*
- C and machine code correctness proofs future work

"Specifications are harder to write than proofs in Coq. Coq will always tell you if the proof is wrong."

(Andrew Appel)

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Pointwise as iterative sum

A pointwise application of a function $f^1 : \mathbb{R} \to \mathbb{R}$ to all elements of vector **a** could be represented as an iterative sum:



Which roughly corresponds to the following loop:

Which requires 4 iterations.

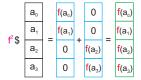
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Pointwise as a vectorized iterative sum

If we have a vectorized implementation of f^1 called $f^2 : \mathbb{R}^2 \to \mathbb{R}^2$ the sum will look like:



Which roughly corresponds to the following loop:

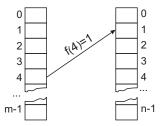
Which now requires only 2 iterations.

Index mapping functions

An index mapping function f has domain of natural numbers \mathbb{N} in interval [0, m) (denoted as \mathbb{I}_m) and the codomain of \mathbb{N} in interval [0, n) (denoted as \mathbb{I}_n):

$$f^{m \to n} : \mathbb{I}_m \to \mathbb{I}_n$$

Such function, for example, could be used to establish relation between indices of two vectors with respective sizes m and n.



Families of Index Mapping Functions

We define a *family* f of k index mapping functions:

$$\forall j < k, \quad f_j^{m \to n} : \mathbb{I}_m \to \mathbb{I}_n \tag{1}$$

The family is called *injective* if it satisfies:

$$\forall n, \forall m, \forall i, \forall j, \quad f_n(i) = f_m(j) \implies (i = j) \land (n = m).$$
(2)

The family is called *surjective* if it satisfies:

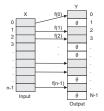
$$\forall j, \exists n, \exists i, f_n(i) = j.$$
(3)

The family is called *bijective* if it is both *injective* and *surjective*.

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Scatter operator

Scatter operator's data flow:



Given an *injective* index mapping function $f^{n \to N}$ the *Scatter* operator $S_f : \mathbb{R}^n \to \mathbb{R}^N$ is defined as:

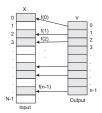
$$\mathbf{y} = \mathsf{S}_{f}(\mathbf{x}) \iff \forall i < n, \ y_{j} = \begin{cases} x_{i} & \exists j < N, \ j = f(i), \\ \theta & otherwise. \end{cases}$$
(4)

Function f must be *injective*. That ensures that every output vector element is assigned exactly once. Additionally, if f is *bijective* it is a *permutation*. If f is a *partial function* some elements of input vector will not be copied to the output.

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Gather operator

Gather operator's data flow:



Given an index mapping function $f^{n \to N}$ the *Gather* operator $G_f : \mathbb{R}^N \to \mathbb{R}^n$ is defined as:

$$\mathbf{y} = \mathsf{G}_f(\mathbf{x}) \iff \forall i < n, \ y_i = x_{f(i)} \tag{5}$$

If *f* is *injective* then every element of input vector will be sent to output vector at most once. Otherwise, some output vector elements can be repeated in the output vector. If *f* is *bijective* and consequently n = N, then *Gather* is a *permutation*.

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Any binary function $f : \mathbb{R} \to \mathbb{R}$ could be lifted to become a standalone operator using Atomic operator:

$$A_f : \mathbb{R}^1 \to \mathbb{R}^1$$

$$[x] \mapsto [f(x)]$$
(6)

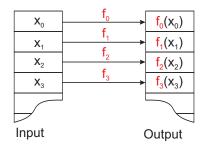
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Pointwise Operator



We define *Pointwise* operator on vectors of dimensionality *n* for a family of functions f_i as:

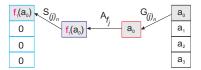
$$P_{f_i}^n : \mathbb{R}^n \to \mathbb{R}^n$$

$$(x_0, x_1, \dots, x_{n-1}) \mapsto (f_0(x_0), f_1(x_1), \dots, f_{n-1}(x_{n-1}))$$
(7)

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Pointwise as iterative sum



It could be shown that *Pointwise* operator could be expressed as a summation:

$$\mathsf{P}_{f_{j}}^{n} = \sum_{j=0}^{n-1} \mathsf{S}_{(j)_{n}} \circ \mathcal{A}_{f_{j}} \circ \mathsf{G}_{(j)_{n}} \tag{8}$$

• Empty elements in sparse vectors are interpreted as zeros

- By $(j)_n$ we denote constant function: $\mathbb{I}_n \to \mathbb{I}_1$ with the value j.
- We will call the summand a Sparse Embedding

Why Structural Correctness Matters

- Dense vectors are decomposed into iterative sums of sparse vectors
- Various decompositions (number or vectors and location of non-sparse values) could represent a variety of memory access patterns.
- This allows applying a variety of algebraic transformations to reshape a computation to optimize for vectorization, parallelization, sequential memory access.
- However in such iterative sum, the addition has a special semantics:
 - Mathematically, the sparse values could be treated as zeroes.
 - Operationally, combining sparse and non-sparse values could be seen as an assignment.
- The expressions produced are naturally mapped to SSA form only if certain constraints on structure of sparse vectors under iterative sums are maintained.
- Tracking and enforcing such constraints for correctness proofs is difficult, as they are not adequately enforced by mathematica abstraction used.

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Sparsity Requirements

In general, the vectors we are dealing with are *sparse*. For example *Scatter* produces a vector with missing values. To prove Σ -HCOL language properties we need our sparse vector formalization to meet the following requirements:

- distinguish empty and assigned cells
- treat empty cells as some "default" value
- such default value could depend on the context (e.g. 0 for addition but 1 for multiplication)
- in case of *SparseEmbedding* we should never attempt to combine two non-sparse elements. This type of error we will call a *collision*
- we would like to separate as much as possible sparsity tracking from actual operations on values as they represent two different aspects of computation

An overview of our sparse vector handing approach:

- Implemented in *Coq* Proof Assistant
- Each value is tagged with two boolean flags: *is_struct* and *is_collision*
- Flags' structure along with combining operator forms a *Monoid*.
- Two monoid instances are used: with and without collision tracking
- Falags are tracked using Writer Monad
- Operations on values could not examine directly sparsity flags and thus could not depend on them
- Sparsity is automatically tracked by the monad. No implicit flags handling in operators' implementation
- Collisions are automatically detected and propagated by the monad

Monoid (Abstract algebra refresher)

A Monoid $(\mathcal{A}, \oplus, \mathbf{0})$ is an algebraic structure which consists of:

- A Set ${\mathcal A}$
- A binary operation $\oplus : \mathcal{A} \to \mathcal{A} \to \mathcal{A}$ (AKA mappend).
- A special set element $\mathbf{0} \in \mathcal{A}$ (AKA *mzero*)

Which satisfy the following *Monoid laws*:

- left identity: $\forall a \in \mathcal{A}, \ \mathbf{0} \oplus a = a$
- right identity: $\forall a \in \mathcal{A}, a \oplus \mathbf{0} = a$
- associativity: $\forall a, b, c \in \mathcal{A}, \ (a \oplus b) \oplus c = a \oplus (b \oplus c)$

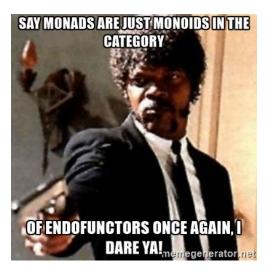
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Flags Monoid

Record $\mathcal{R}_{\mathrm{flags}}$: Type := mk $\mathcal{R}_{\mathrm{flags}}$ {is_struct: \mathbb{B} ; is_collision: \mathbb{B} }.

The initial flags' value has structural flag *True* and collision flag *False*. The *mappend* operation combines the two sets of flags as follows. If one of operands is non-structural, the result is also non-structural. The collision flags are "sticky". Combining two non-structural elements, causes a collision. It could be proven that *monoid laws* are satisfied.

What is a Monad?



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Monad intuition

See "Monad Tutorial Fallacy" http://bit.ly/monads-are-burritos



"Monads apply a function that returns a wrapped value to a wrapped value." $^{\rm 2}$

²Image credit: http://adit.io/posts/2013-04-17-functors,_applicatives,_and_monads_in_pictures.html= -> ? ? ?

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Monad in Coq

A simplified³ definition of *Monad* class from *Coq ExtLib*:

m is called a *type constructor*ret "wraps" a value into a monad
bind takes a wrapped value, a function which returns a wrapped value

³Removed universe polymorphism

WriterMonad

- One can think about WriterMonad as a product type t × s containing a value of type t and a state of type s. The state must be a Monoid.
- Monadic *ret* function constructs the new *WriterMonad* value by combining provided value with *mzero* state.
- Monadic *bind* operator allows to combine monadic values using user-provided functoin, and takes care of state tracking combining states via *mappend*.
- In additon to *ret* and *bind* the following writer-specific functions are defined:

```
writer: \forall s: Type, Monoid s \rightarrow Type \rightarrow Type
tell: \forall (s: Type) (w: Monoid s), s \rightarrow writer w ()
runWriter: \forall (s t : Type) (w: Monoid s), writer w t \rightarrow t×s
execWriter: \forall (s t : Type) (w : Monoid s), writer w t \rightarrow s
evalWriter: \forall (s t : Type) (w : Monoid s), writer w t \rightarrow t
```

Combining $\mathcal{R}_{\mathrm{flags}}$ and $\mathit{WriterMonad}$

To track the flags while performing operations on \mathbb{R} values we will use Writer Monad, parametrized by a Monoid which defines how flags will be handled:

Definition $\mathcal{R}_{\theta} :=$ writer Monoid_ $\mathcal{R}_{\text{flags}} \mathbb{R}$.

To construct values of the type \mathcal{R}_{θ} we define two convenience functions:

Definition mkStruct (v: \mathbb{R}) : \mathcal{R}_{θ} := ret v. Definition mkValue (v: \mathbb{R}) : \mathcal{R}_{θ} := tell (mk $\mathcal{R}_{\mathrm{flags}} \perp \perp$) ;; ret v.

Any unary or binary operation could be "lifted" to operate on monadic values using *liftM* or *liftM2* respectively:

Sparse Operator Example

Now we can define an operator:

Key points:

- actual operation performing computations (f) is defined on $\mathbb R$
- all structural flags tracking is transparent
- a raw vector x could be passed as an argument by lifting it via (vector.map ret x)
- a vector of raw values could be extracted from the result x by simply applying (vector.map evalWriter x).
- Correctness condition on the resulting vector x could checked using:

Definition vecNoCollision {n: \mathbb{N} } (v: vector \mathcal{R}_{θ} n) : Prop := vector.Forall (not \circ is_collision \circ execWriter) v

Iterative Operators – from dense to sparse

We have shown earlier that *Pointwise* operator on \mathbb{R}^n could be expressed as a summation:

$$\mathsf{P}_{f_j}^n x = \sum_{j=0}^{n-1} \left(\mathsf{S}_{(j)_n} \circ \mathcal{A}_{f_j} \circ \mathsf{G}_{(j)_n} x \right)$$

- This formulation was using dense vectors, without collision tracking. Now we would like to extend it to sparse vectors with collision tracking.
- It was using summation to combine elements. We would like to generalize it to other operations such as multiplication.
- We would like to generalize this notation to *iterative operators* using pointfree notation.

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Similarly to as how we defined a family of index functions earlier we define a *family* F of k operators:

$$\forall j < k, \quad F_j : \ \mathcal{B}^m \to \mathcal{D}^n \tag{9}$$

- All operators in the family have the same type.
- Instead of \mathbb{R} we use abstract types \mathcal{B} and \mathcal{D} .
- In subsequent slides we will use uppercase calligraphic letters to denote abstract types.

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Scalar, Vector, and Operator Diamond

From arbitrary binary operation $\diamond: \mathcal{A} \to \mathcal{A} \to \mathcal{A}$ we can induce binary pointwise vector diamond operation:

$$\vec{\diamond} : \mathcal{A}^{n} \to \mathcal{A}^{n} \to \mathcal{A}^{n}$$

$$((a_{0}, a_{1}, \dots, a_{n-1}), (b_{0}, b_{1}, \dots, b_{n-1})) \mapsto (10)$$

$$(a_{0} \diamond b_{0}, a_{1} \diamond b_{1}, \dots, a_{n-1} \diamond b_{n-1})$$

Next, we can define operator diamond:

$$\overset{\diamond}{\circ} : (\mathcal{A}^{n} \to \mathcal{A}^{m}) \to (\mathcal{A}^{n} \to \mathcal{A}^{m}) \to (\mathcal{A}^{n} \to \mathcal{A}^{m}) (F, G) \mapsto (\mathbf{x} \mapsto F(\mathbf{x}) \, \vec{\diamond} \, G(\mathbf{x}))$$
(11)

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Iterative Diamond

Operator diamond in turn induces an *iterative diamond* operation for a family of *n* operators $F : \mathcal{A}^n \to \mathcal{A}^n$:

$$\bigotimes_{i=0}^{n-1} F_i = F_1 \diamond F_2 \diamond \cdots \diamond F_n$$

Or more formally, the recursive definition:

$$\begin{split} & \bigoplus_{i=0}^{n-1} F_i : \mathcal{A}^n \to \mathcal{A}^n \\ & \mathbf{x} \mapsto \begin{cases} \mathbf{0}^n & \text{if } n = 0, \\ \left(F_{n-1} \diamond \begin{pmatrix} n-2 \\ \diamondsuit \\ j=0 \end{cases} F_j \end{pmatrix} \right) (\mathbf{x}) & \text{otherwise.} \end{split}$$
 (12)

An additional requirement here is that the Set \mathcal{A} forms a *Monoid* with identity element 0 of type \mathcal{A} and binary associative operation $\diamond : \mathcal{A} \to \mathcal{A} \to \mathcal{A}$. The notation $\mathbf{0}^n$ denotes constant vector of identity elements of length *n*.

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Let us apply the *diamond* abstraction demonstrated in previous slides to \mathcal{R}_{θ} type (which represents \mathbb{R} values with $\mathcal{R}_{\mathrm{flags}}$ state) and summation operator. To do so we specalize previous notation as follows:

Definition
$$\mathcal{A} := \mathcal{R}_{\theta}$$
.
Definition $\diamond := \texttt{liftM2}(+)$.
Definition $\vec{\diamond} := \texttt{vector.map2} \diamond$. (* generic *)
Definition $\diamond \texttt{f} \texttt{g} := \lambda \texttt{x} \Rightarrow (\texttt{f} \texttt{x}) \vec{\diamond} (\texttt{g} \texttt{x})$. (* generic *)
Definition $\mathbf{0}^n := \texttt{vector.const}(\texttt{ret 0}) \texttt{n}$.

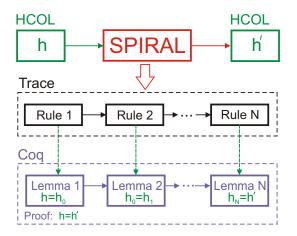
This gives us a sparse, collision-tracking Pointwise:

$$\mathsf{Pointwise}_{n,f} = \bigotimes_{j=0}^{n-1} \left(\mathsf{S}_{(j)_n} \circ \mathsf{A}_{f_j} \circ \mathsf{G}_{(j)_n} \right) \tag{13}$$

- SPIRAL performs a series of program transformations in HCOL and Σ-COL languages. These transformations needs to be semantic-preserving.
- **2** The final Σ -HCOL expression must have certain structural properties
- Sigma-HCOL to i-Code translation needs to preserve semantics.
- i-Code is then compiled to LLVM or C and this compilation must preserve semantics.
- The final compilation of *LLVM* or *C* will be performed by a verified compiler such as *CompCert* or *VELLVM*

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Proving Rewriting Rules



- "Translation Validation" vs full compiler verification approach.
- Each Σ-HCOL rewriting rule is a lemma.

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Proving Rewriting Rules – Value Correctness

- Abstracting \mathbb{R} as a *carrier data type*
- Using Setoid equality relation defined on carrier type
- Comparison unwraps the *WriterMonad* and compare just values, ignoring state (flags)
- Operators are functions on vectors
- Per-rule lemmas stating Extensional Setoid Equality of (compound) operators.
- Mixed embedding: Record containing operator function as well as additional properties such as *Setoid Morphism* (*Proper* instance)
- Value correctness reasoning in transitional.

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Proving Rewriting Rules – Structural Properties

- Structural properties deal with sparsity flags and collision errors only. (They only examine the state of the *Writer Monad*).
- Operator record is extended to include finite sets of indices of non-sparse value of input and output vectors.
- The properties itselfs are expressed as a typeclass all operators must be instances of. The properties are:
 - Both in_index_set and out_index_set memberships are decideabe
 - Only input elements with indices in *in_index_set* affect output
 - Sufficiently (values in right places, no info on empty spaces) filled input vector guarantees properly (values are only where values expected) filled output vector
 - Sever generate values at sparse positions of output vector
 - As long there are no collisions in expected non-sparse places, none is expected in nonsparce places on output
 - O Never generate collisions on sparse places
- Structural correctness reasoning in *compositional*!

Summary

What have been formalized so far in Cog:

- Index functions and their families.
- Σ-HCOL operators and their families
- Sparse vectors
- Operator equality
- Operator structural properties
- Generalized notion of iterative operators

Next steps:

- Σ -HCOL rewriting proof automation using.
- Formalzing *i-Code* (operational semantics)
- Linking HCOL semantics to *i-Code* semantics
- Linking *i-Code* semantics to C or LLVM semantics

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For Further Reading I



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IEEE Control Systems, 2017

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