



Operator Language: A Program Generation Framework for Fast Kernels

Franz Franchetti, Frédéric de Mesmay, Daniel McFarlin, Markus Püschel

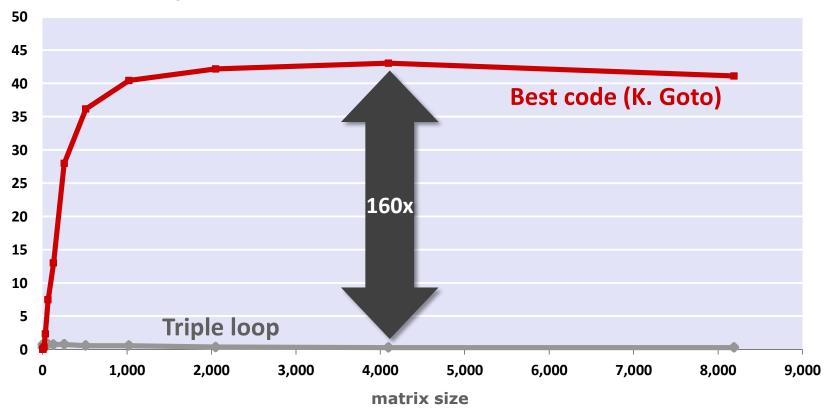
Electrical and Computer Engineering Carnegie Mellon University

Sponsors: DARPA DESA program, NSF-NGS/ITR, NSF-ACR, and Intel

SPIRAL www.spiral.net

The Problem: Example MMM

Matrix-Matrix Multiplication (MMM) on 2xCore2Duo 3 GHz (double precision) Performance [Gflop/s]



- Similar plots can be shown for all numerical kernels in linear algebra, signal processing, coding, crypto, ...
- What's going on? Hardware is becoming increasingly complex.



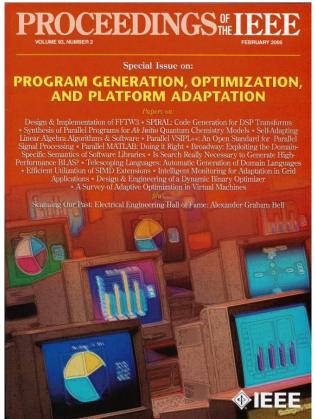
Automatic Performance Tuning

Current vicious circle: Whenever a new platform comes out, the same functionality needs to be rewritten and reoptimized

Automatic Performance Tuning

- BLAS: ATLAS, PHiPAC
- Linear algebra: Sparsity/OSKI, Flame
- Sorting
- Fourier transform: FFTW
- Linear transforms (and beyond): Spiral
- …others

How to build an extensible system? For more problem classes? For yet un-invented platforms?

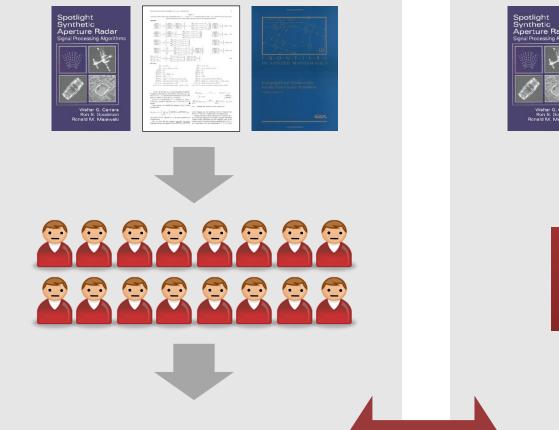


Proceedings of the IEEE special issue, Feb. 2005

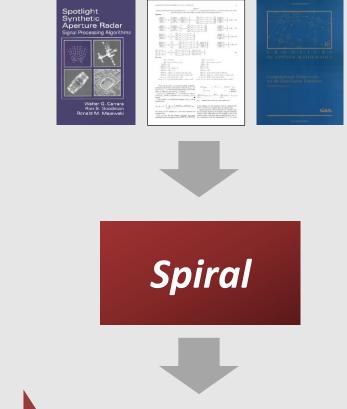


What is Spiral?

Traditionally



Spiral Approach



High performance library optimized for given platform

Comparable performance

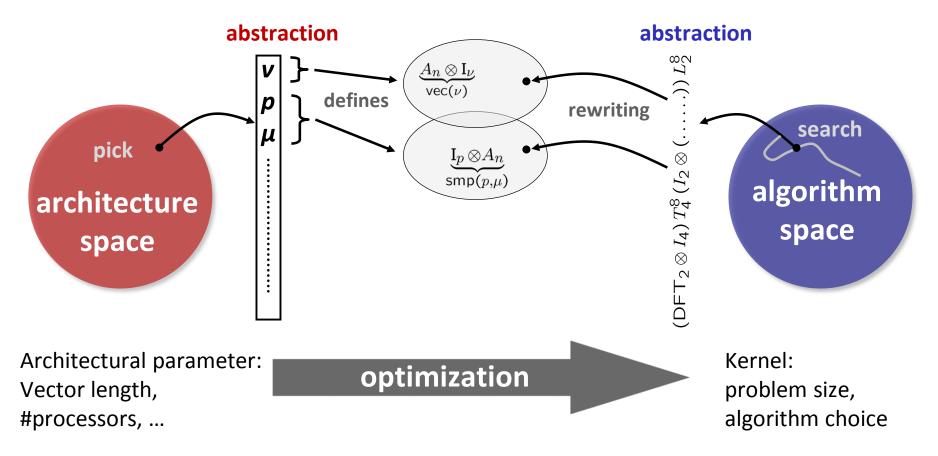
High performance library optimized for given platform

SPIR.

Idea: Common Abstraction and Rewriting

Model: common abstraction

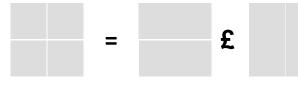
- = spaces of matching formulas
 - = domain-specific language



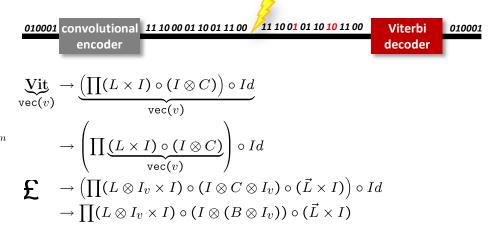
Some Kernels as OL Formulas.

Linear Transforms

Matrix-Matrix Multiplication



Viterbi Decoding



Synthetic Aperture Radar (SAR)

→ preprocessing → matched filtering → interpolation → 2D iFFT →

$$\begin{array}{rcl} \mathsf{SAR}_{k \times m \to n \times n} & \to & \mathsf{DFT}_{n \times n} \circ \mathsf{Interp}_{k \times m \to n \times n} \\ & \mathsf{DFT}_{n \times n} & \to & (\mathsf{DFT}_n \otimes \mathrm{I}_n) \circ (\mathrm{I}_n \otimes \mathsf{DFT}_n) \end{array}$$
$$\begin{array}{rcl} \mathsf{Interp}_{k \times m \to n \times n} & \to & (\mathsf{Interp}_{k \to n} \otimes_i \mathrm{I}_n) \circ (\mathrm{I}_k \otimes_i \mathsf{Interp}_{m \to n}) \\ & \mathsf{Interp}_{r \to s} & \to & \left(\bigoplus_{i=0}^{n-2} \mathsf{InterpSeg}_k \right) \oplus \mathsf{InterpSegPruned}_{k,\ell} \\ & \mathsf{InterpSeg}_k & \to & \mathsf{G}_f^{u \cdot n \to k} \circ \mathsf{iPrunedDFT}_{n \to u \cdot n} \circ \left(\frac{1}{n} \right) \circ \mathsf{DFT}_n \end{array}$$



How Spiral Works

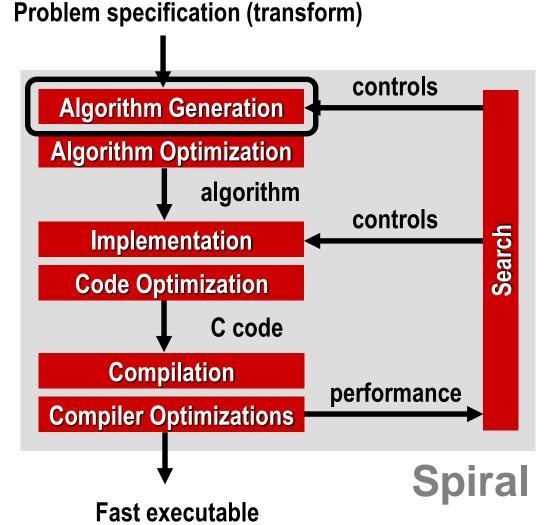
Spiral:

Complete automation of the implementation and optimization task

Basic ideas:

Declarative representation of algorithms

Rewriting systems to generate and optimize algorithms at a high level of abstraction



Markus Püschel, José M. F. Moura, Jeremy Johnson, David Padua, Manuela Veloso, Bryan Singer, Jianxin Xiong, Franz Franchetti, Aca Gacic, Yevgen Voronenko, Kang Chen, Robert W. Johnson, and Nick Rizzolo: **SPIRAL: Code Generation for DSP Transforms.** Special issue, Proceedings of the IEEE 93(2), 2005



Organization

- Operator language and algorithms
- Optimizing algorithms for platforms
- Performance results
- Summary



Organization

- Operator language and algorithms
- Optimizing algorithms for platforms
- Performance results
- Summary



Operators

Definition

- Operator: Multiple complex vectors ! multiple complex vectors
- Higher-dimensional data is linearized
- Operators are potentially nonlinear

$$\mathsf{M}: \begin{cases} \mathbb{C}^{n_0} \times \cdots \times \mathbb{C}^{n_{k-1}} \to \mathbb{C}^{N_0} \times \cdots \times \mathbb{C}^{N_{\ell-1}} \\ (\mathbf{x}_0, \mathbf{x}_1, \dots, \mathbf{x}_{k-1}) \mapsto \mathsf{M}(\mathbf{x}_0, \mathbf{x}_1, \dots, \mathbf{x}_{k-1}) \end{cases}$$

Example: Matrix-matrix-multiplication (MMM)

$$\mathbf{MMM}_{m,k,n} : \mathbb{R}^{mk} \times \mathbb{R}^{kn} \to \mathbb{R}^{mn}$$

$$(\mathbf{A}, \mathbf{B}) \mapsto \mathbf{AB}$$

$$n \qquad \mathbf{A}$$

$$n \qquad \mathbf{MMM}_{m,k,n} \xrightarrow{n} \mathbf{C} m$$

$$k \qquad \mathbf{B}$$

$$(\mathbf{MMM}_{m,k,n} \xrightarrow{n} \mathbf{C} m$$

SPIRAI

Operator Language

name

definition

Linear, arity (1,1)identity vector flip transposition of an $m \times n$ matrix matrix $M \in \mathbb{C}^{m \times n}$ Multilinear, arity (2,1) Point-wise product Scalar product Kronecker product Others Fork Split Concatenate Duplication Min Max

$I_n: \mathbb{C}^n \to \mathbb{C}^n; \mathbf{x} \mapsto \mathbf{x}$
$J_n: \mathbb{C}^n \to \mathbb{C}^n; \ (x_i) \mapsto (x_{n-i})$
$L_m^{mn}: \mathbb{C}^{mn} \to \mathbb{C}^{mn}; \mathbf{A} \mapsto \mathbf{A}^T$
$\mathbf{M}: \mathbb{C}^n \to \mathbb{C}^m; \mathbf{x} \mapsto M\mathbf{x}$

$$P_n : \mathbb{C}^n \times \mathbb{C}^n \to \mathbb{C}^n; ((x_i), (y_i)) \mapsto (x_i y_i)$$

$$S_n : \mathbb{C}^n \times \mathbb{C}^n \to \mathbb{C}; ((x_i), (y_i)) \mapsto \Sigma(x_i y_i)$$

$$K_{m \times n} : \mathbb{C}^m \times \mathbb{C}^n \to \mathbb{C}^{mn}; ((x_i), \mathbf{y})) \mapsto (x_i \mathbf{y})$$

Fork_n: $\mathbb{C}^n \to \mathbb{C}^n \times \mathbb{C}^n$; $\mathbf{x} \mapsto (\mathbf{x}, \mathbf{x})$ Split_n: $\mathbb{C}^n \to \mathbb{C}^{n/2} \times \mathbb{C}^{n/2}$; $\mathbf{x} \mapsto (\mathbf{x}^U, \mathbf{x}^L)$ $\oplus_n : \mathbb{C}^n \times \mathbb{C}^m \to \mathbb{C}^{n+m}$; $(\mathbf{x}, \mathbf{y}) \mapsto \mathbf{x} \oplus \mathbf{y}$ dup_n^m: $\mathbb{C}^n \to \mathbb{C}^{nm}$; $(\mathbf{x} \mapsto \mathbf{x} \otimes I_m)$ min_n: $\mathbb{C}^n \times \mathbb{C}^n \to \mathbb{C}^n$; $(\mathbf{x}, \mathbf{y}) \mapsto (\min(x_i, y_i))$ max_n: $\mathbb{C}^n \times \mathbb{C}^n \to \mathbb{C}^n$; $(\mathbf{x}, \mathbf{y}) \mapsto (\max(x_i, y_i))$



OL Tensor Product: Repetitive Structure

Kronecker product

(structured matrices)

$$\mathbf{I}_m \otimes A_n = \begin{bmatrix} A_n & & \\ & \ddots & \\ & & A_n \end{bmatrix}$$

OL Tensor product

(structured operators)

$$\mathbf{I}_{M/M_B \times 1 \to M/M_B} \otimes \mathsf{MMM}_{M_B,N,K} = \left((A,B) \mapsto \left[\frac{A_0}{\frac{1}{A_{M/M_B} - 1}} \right] B \right)$$

Definition

(extension to non-linear)

$$(\mathbf{I}_{m \times n \to mn} \otimes \mathsf{A}) \begin{pmatrix} m-1 \\ \sum \\ i=0 \end{pmatrix} \mathbf{e}_{i}^{m} \otimes \mathbf{x}, \sum _{i=0}^{n-1} \mathbf{e}_{i}^{n} \otimes \mathbf{y} \end{pmatrix} = \sum_{i=0}^{m-1} \sum_{j=0}^{n-1} \mathbf{e}_{i}^{m} \otimes \mathbf{e}_{j}^{n} \otimes \mathsf{A}(\mathbf{x}, \mathbf{y})$$
$$(\mathsf{A} \otimes \mathbf{I}_{m \times n \to mn}) \begin{pmatrix} m-1 \\ \sum \\ i=0 \end{pmatrix} \mathbf{x} \otimes \mathbf{e}_{i}^{m}, \sum_{i=0}^{n-1} \mathbf{y} \otimes \mathbf{e}_{i}^{n} \end{pmatrix} = \sum_{i=0}^{m-1} \sum_{j=0}^{n-1} \mathsf{A}(\mathbf{x}, \mathbf{y}) \otimes \mathbf{e}_{i}^{m} \otimes \mathbf{e}_{j}^{n}$$



Translating OL Formulas Into Programs

operator formula	code
$\mathbf{r} = (A_{K \times M \to N} \circ B_{k \times m \to K \times M})(\mathbf{x}, \mathbf{y})$	(t, u) = B(x, y);
	r = A(t, u);
$(\mathbf{r},\mathbf{s}) = (A_{m \to M} \times B_{n \to N})(\mathbf{x},\mathbf{y})$	r = A(x);
	s = B(y);
/- · · · · · · · · · · · · · · · · · · ·	for (i=0;i <m;i++)< td=""></m;i++)<>
$\mathbf{r} = (\mathrm{I}_{m imes n ightarrow mn} \otimes A_{M imes N ightarrow K})(\mathbf{x}, \mathbf{y})$	for (j=0;j <m;j++)< td=""></m;j++)<>
	r[(i*m+j)*K:1:(i*m+j+1)*K-1] =
	A(x[i*M:1:i*M+M-1], y[j*N:1:j*N+N-1]);
	for (i=0;i <m;i++)< td=""></m;i++)<>
$\mathbf{r} = (A_{M imes N ightarrow K} \otimes \mathbf{I}_{m imes n ightarrow mn})(\mathbf{x}, \mathbf{y})$	for (j=0;j <n;j++)< td=""></n;j++)<>
	r[i*m+j:m*n:i*m+j+m*n*(K-1))] =
	A(x[i:m:i+m*(M-1))], y[j:n:j+n*(N-1)]);

$$\mathbf{I}_{M/M_B \times 1 \to M/M_B} \otimes \mathsf{MMM}_{M_B,N,K} = \left((A,B) \mapsto \left[\frac{A_0}{\frac{\mathbf{i}}{A_{M/M_B} - 1}} \right] B \right)$$



Example: Matrix Multiplication (MMM)

Breakdown rules:

capture various forms of blocking

breakdown rule	description
$MMM_{1,1,1} \to (\cdot)_1$	base case
$MMM_{m,n,k} o (\otimes)_{m/m_b imes 1} \otimes MMM_{m_b,n,k}$	horizontal blocking
$MMM_{m,n,k} \to MMM_{m,nb,k} \otimes (\otimes)_{1 \times n/nb}$	interleaved blocking $=$
$egin{aligned} MMM_{m,n,k} & ightarrow ((\mathbf{\Sigma}_{k/k_b} \circ (\cdot)_{k/k_b}) \otimes MMM_{m,n,k_b}) \circ & ((L_{k/k_b}^{mk/k_b} \otimes I_{k_b}) imes I_{kn}) & \ & ((L_{k/k_b}^{mn/n_b} \otimes I_{n_b}) \circ & \ & \ & \ & \ & \ & \ & \ & \ & \ &$	accumulative blocking $=$
$(I_{km}^{m,n,k} \rightarrow (L_{m}^{m} \otimes I_{n_{b}}) \circ ((\otimes)_{1 \times n/n_{b}} \otimes MMM_{m,n_{b},k}) \circ (I_{km} \times (L_{n/n_{b}}^{kn/n_{b}} \otimes I_{n_{b}}))$	vertical blocking



SPIRA www.spiral.ne **Example: SAR Computation as OL Rules** Grid Compute Range Interpolation Azimuth Interpolation $\mathsf{SAR}_{k \times m \to n \times n} \to \mathsf{DFT}_{n \times n} \circ \mathsf{Interp}_{k \times m \to n \times n}$ $\mathsf{DFT}_{n \times n} \rightarrow (\mathsf{DFT}_n \otimes \mathsf{I}_n) \circ (\mathsf{I}_n \otimes \mathsf{DFT}_n)$ $\operatorname{Interp}_{k \times m \to n \times n} \to (\operatorname{Interp}_{k \to n} \otimes_i \operatorname{I}_n) \circ (\operatorname{I}_k \otimes_i \operatorname{Interp}_{m \to n})$ $\operatorname{Interp}_{r \to s} \to \left(\bigoplus_{i=0}^{n-2} \operatorname{InterpSeg}_k \right) \oplus \operatorname{InterpSegPruned}_{k,\ell}$ $\mathsf{InterpSeg}_k \to \mathsf{G}_f^{u \cdot n \to k} \circ \mathsf{iPrunedDFT}_{n \to u \cdot n} \circ \left(\frac{1}{2}\right) \circ \mathsf{DFT}_n$



Organization

- Operator language and algorithms
- Optimizing algorithms for platforms
- Performance results
- Summary





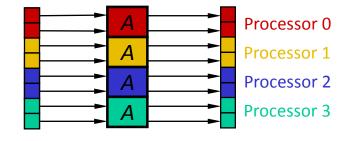
Modeling Multicore: Base Cases

Hardware abstraction: shared cache with cache lines



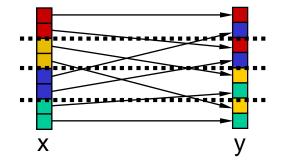
Tensor product: embarrassingly parallel operator

$$y = (\mathbf{I}_p \otimes A)(x)$$



Permutation: problematic; may produce false sharing

$$y = L_4^8(x)$$





Parallelization: OL Rewriting Rules

- Tags encode hardware constraints
- Rules are algorithm-independent
- Rules encode program transformations

$$\underbrace{\begin{pmatrix} \mathbf{I}_{k} \otimes \mathbf{L}_{n}^{mn} \\ \operatorname{smp}(p,\mu) \end{pmatrix}}_{\operatorname{smp}(p,\mu)} \circ \underbrace{\mathbf{L}_{km}^{kmn}}_{\operatorname{smp}(p,\mu)} \to \left(\mathbf{L}_{k}^{kn} \otimes \mathbf{I}_{m/\mu} \right) \bar{\otimes} \mathbf{I}_{\mu} \\ \underbrace{\mathbf{L}_{n}^{kmn}}_{\operatorname{smp}(p,\mu)} \circ \underbrace{\begin{pmatrix} \mathbf{I}_{k} \otimes \mathbf{L}_{m}^{mn} \\ \operatorname{smp}(p,\mu) \end{pmatrix}}_{\operatorname{smp}(p,\mu)} \to \left(\mathbf{L}_{n}^{kn} \otimes \mathbf{I}_{m/\mu} \right) \bar{\otimes} \mathbf{I}_{\mu} \\ \underbrace{\mathbf{A} \circ \mathbf{B}}_{\operatorname{smp}(p,\mu)} \to \underbrace{\mathbf{A} \circ \mathbf{B}}_{\operatorname{smp}(p,\mu)} \circ \underbrace{\mathbf{B}}_{\operatorname{smp}(p,\mu)} \circ \underbrace{\mathbf{A}^{k \times m \to n} \otimes \mathbf{I}^{1 \times p \to p}}_{\operatorname{smp}(p,\mu)} \to \underbrace{\mathbf{L}_{n}^{pn}}_{\operatorname{smp}(p,\mu)} \circ \left(\mathbf{I}_{1 \times p \to p} \otimes_{\parallel} \mathbf{A}^{k \times m \to n} \right) \circ \underbrace{\left(\mathbf{I}_{k} \times \mathbf{L}_{p}^{pm} \right)}_{\operatorname{smp}(p,\mu)} \\ \underbrace{\left(\mathbf{A} \times \mathbf{B} \right)}_{\operatorname{smp}(p,\mu)} \circ \underbrace{\left(\mathbf{C} \times \mathbf{D} \right)}_{\operatorname{smp}(p,\mu)} \to \underbrace{\left(\mathbf{A} \circ \mathbf{C} \right)}_{\operatorname{smp}(p,\mu)} \times \underbrace{\left(\mathbf{B} \circ \mathbf{D} \right)}_{\operatorname{smp}(p,\mu)}$$



The Joint Rule Set: MMM

Algorithm rules: breakdown rules

Hardware constraints: base cases

 $I_{p} \otimes_{\parallel} A_{m\mu \to n\mu}, \quad \mathsf{P}_{p} \otimes_{\parallel} A_{k\mu \times m\mu \to n\mu}, \quad \mathsf{K}_{q \times r} \otimes_{\parallel} A_{k\mu \times m\mu \to n\mu}, \\ M \bar{\otimes} \mu, \quad (M \bar{\otimes} I_{\mu}) \otimes (N \bar{\otimes} I_{\mu})$

Program transformations: manipulation rules

$$\underbrace{\begin{pmatrix} \mathbf{I}_{k} \otimes \mathbf{L}_{n}^{mn} \\ \mathrm{smp}(p,\mu) \end{pmatrix}}_{\substack{\mathsf{smp}(p,\mu) \\ \mathsf{smp}(p,\mu) \\ \mathsf{smp}(p,\mu) \\ \mathsf{smp}(p,\mu) \\ mp(p,\mu) \\ \mathbf{smp}(p,\mu) \\ mp(p,\mu) \\ \mathbf{smp}(p,\mu) \\ \mathbf$$

Combined rule set spans search space for empirical optimization



$$\begin{split} \underbrace{\mathsf{MMM}_{m,n,k}}_{\mathsf{smp}(p,\mu)} \\ \to \underbrace{\left(\mathbf{I}_m \otimes \mathbf{L}_p^n\right) \circ \left(\mathsf{MMM}_{m,n/p,k} \otimes (\otimes)_{1 \times p \to p}\right) \circ \left(\mathbf{I}_{km} \times (\mathbf{I}_k \otimes \mathbf{L}_{n/p}^n)\right)}_{\mathsf{smp}(p,\mu)} \\ \to \underbrace{\left(\mathbf{I}_m \otimes \mathbf{L}_p^n\right)}_{\mathsf{smp}(p,\mu)} \circ \underbrace{\left(\mathsf{MMM}_{m,n/p,k} \otimes (\otimes)_{1 \times p \to p}\right)}_{\mathsf{smp}(p,\mu)} \circ \underbrace{\left(\mathbf{I}_{km} \times (\mathbf{I}_k \otimes \mathbf{L}_{n/p}^n)\right)}_{\mathsf{smp}(p,\mu)} \\ \to \underbrace{\left(\mathbf{I}_m \otimes \mathbf{L}_p^n\right)}_{\mathsf{smp}(p,\mu)} \circ \underbrace{\left(\mathsf{MMM}_{m,n/p,k} \otimes (\otimes)_{1 \times p \to p} \otimes_{\parallel} \mathsf{MMM}_{m/p,n,k}\right) \circ \underbrace{\left(\mathbf{I}_{km} \times \mathbf{L}_p^{kn}\right)}_{\mathsf{smp}(p,\mu)} \circ \underbrace{\left(\mathbf{I}_{km} \times (\mathbf{I}_k \otimes \mathbf{L}_{n/p}^n)\right)}_{\mathsf{smp}(p,\mu)} \\ \to \underbrace{\left((\mathbf{L}_m^{mp} \otimes \mathbf{I}_{n/(p\mu)}) \otimes \mathbf{I}_{\mu}\right) \circ \left((\otimes)_{1 \times p \to p} \otimes_{\parallel} \mathsf{MMM}_{m,n/p,k}\right) \circ \underbrace{\left(\mathbf{I}_{km/\mu} \otimes \mathbf{I}_{\mu}\right) \times \left((\mathbf{L}_p^{kp} \otimes \mathbf{I}_{n/(p\mu)}) \otimes \mathbf{I}_{\mu}\right)}_{\mathsf{smp}(p,\mu)} \end{split}$$

Load-balanced No false sharing

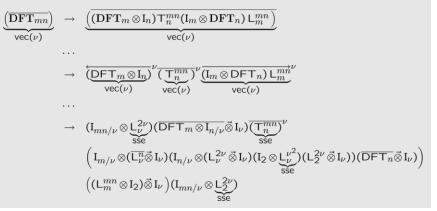


Same Approach for Different Paradigms

Threading:

$\underbrace{\mathbf{DFT}_{mn}}_{smp(p,\mu)}$	\rightarrow	$\frac{\left((\mathbf{DFT}_m \otimes \mathbf{I}_n)T_n^{mn}(\mathbf{I}_m \otimes \mathbf{DFT}_n)L_m^{mn}\right)}{smp(p,\mu)}$
	• • •	
	\rightarrow	$\underbrace{\left(\mathbf{DFT}_{m}\otimes\mathbf{I}_{n}\right)}_{smp(p,\mu)}\underbrace{T_{n}^{mn}}_{smp(p,\mu)}\underbrace{\left(\mathbf{I}_{m}\otimes\mathbf{DFT}_{n}\right)}_{smp(p,\mu)}\underbrace{L_{m}^{nm}}_{smp(p,\mu)}$
	• • •	
	\rightarrow	$\left((L_m^{mp} \otimes \mathrm{I}_{n/p\mu}) \otimes_{\mu} \mathrm{I}_{\mu}\right) \left(\mathrm{I}_p \otimes_{\parallel} (\mathbf{DFT}_m \otimes \mathrm{I}_{n/p})\right) \left((L_p^{mp} \otimes \mathrm{I}_{n/p\mu}) \otimes_{\mu} \mathrm{I}_{\mu}\right)$
		$\left(\bigoplus_{i=0}^{p-1} T_n^{mn,i}\right) \Big(\mathrm{I}_p \otimes_{\parallel} (\mathrm{I}_{m/p} \otimes \mathbf{DFT}_n) \Big) \Big(\mathrm{I}_p \otimes_{\parallel} L_{m/p}^{mn/p} \Big) \Big((L_p^{pn} \otimes \mathrm{I}_{m/p\mu}) \otimes_{\mu} \mathrm{I}_{\mu} \Big)$

Vectorization:



$$(DFT_{rk})_{gpu(t,c)} \rightarrow \underbrace{\left(\prod_{i=0}^{k-1} L_r^{rk} \left(I_{rk-1} \otimes DFT_r \right) \left(L_{rk-i-1}^{rk} (I_{ri} \otimes T_{rk-i-1}^{rk-i}) \underbrace{L_{ri+1}^{rk}}_{vec(c)} \right) \right)}_{gpu(t,c)} \\ \dots \\ \rightarrow \underbrace{\left(\prod_{i=0}^{k-1} (L_r^{rn/2} \otimes I_2) \left(I_{rn-1/2} \otimes \times \underbrace{(DFT_r \otimes I_2) L_r^{2r}}_{shd(t,c)} \right) T_i \right)}_{(L_r^{rn/2} \otimes I_2) (I_{rn-1/2} \otimes \times \underbrace{L_r^{2r}}_{shd(t,c)}) (R_r^{rn-1} \otimes I_r)}$$

Verilog for FPGAs:

$$\begin{split} \underbrace{\left(\mathbf{DFT}_{rk}\right)}_{\mathsf{stream}(r^{s})} & \rightarrow \underbrace{\left[\prod_{i=0}^{k-1} \mathsf{L}_{r}^{r^{k}} \left(\mathbf{I}_{r^{k-1}} \otimes \mathbf{DFT}_{r}\right) \left(\mathsf{L}_{r^{k-i-1}}^{r^{k}} (\mathbf{I}_{r^{i}} \otimes \mathsf{T}_{r^{k-i-1}}^{r^{k-i}}) \mathsf{L}_{r^{i+1}}^{r^{k}}\right)\right] \mathsf{R}_{r}^{r^{k}}}_{\mathsf{stream}(r^{s})} \\ & \cdots \\ & \rightarrow \underbrace{\left[\prod_{i=0}^{k-1} \underbrace{\mathsf{L}_{r}^{r^{k}}}_{\mathsf{stream}(r^{s})} \underbrace{\left(\mathbf{I}_{r^{k-1}} \otimes \mathbf{DFT}_{r}\right)}_{\mathsf{stream}(r^{s})} \underbrace{\left(\mathsf{L}_{r^{k-i-1}}^{r^{k}} (\mathbf{I}_{r^{i}} \otimes \mathsf{T}_{r^{k-i-1}}^{r^{k-i}}) \mathsf{L}_{r^{i+1}}^{r^{k}}\right)}_{\mathsf{stream}(r^{s})}\right] \underbrace{\mathsf{R}_{r}^{r^{k}}}_{\mathsf{stream}(r^{s})} \\ & \cdots \\ & \rightarrow \underbrace{\left[\prod_{i=0}^{k-1} \underbrace{\mathsf{L}_{r}^{r^{k}}}_{\mathsf{stream}(r^{s})} \left(\mathbf{I}_{r^{k-1}} \otimes \mathbf{DFT}_{r}\right) \otimes \mathsf{DFT}_{r}\right) \underbrace{\mathsf{L}_{r^{i}}^{r^{k}}}_{\mathsf{stream}(r^{s})}\right] \underbrace{\mathsf{R}_{r}^{r^{k}}}_{\mathsf{stream}(r^{s})} \\ & \overset{(\mathsf{L}_{r^{k-1}} \otimes \mathbf{DFT}_{r})}_{\mathsf{stream}(r^{s})} \underbrace{\mathsf{L}_{r^{i}}^{r^{k}}}_{\mathsf{stream}(r^{s})} \underbrace{\mathsf{R}_{r}^{r^{k}}}_{\mathsf{stream}(r^{s})}\right] \\ & \overset{(\mathsf{L}_{r^{k}} \otimes \mathsf{L}_{r^{k}})}_{\mathsf{stream}(r^{s})} \underbrace{\mathsf{R}_{r^{k}}}_{\mathsf{stream}(r^{s})} \underbrace{\mathsf{R}_{r^{k}}^{r^{k}}}_{\mathsf{stream}(r^{s})} \underbrace{\mathsf{R}_{r^{k}}^{r^{k}}}_{\mathsf{stream}(r^{s})} \underbrace{\mathsf{R}_{r^{k}}^{r^{k}}}_{\mathsf{stream}(r^{s})} \underbrace{\mathsf{R}_{r^{k}}^{r^{k}}}_{\mathsf{stream}(r^{s})} \underbrace{\mathsf{R}_{r^{k}}^{r^{k}}}}_{\mathsf{stream}(r^{s})} \underbrace{\mathsf{R}_{r^{k}}^{r^{k}}}_{\mathsf{stream}(r^{s})} \underbrace{\mathsf{R}_{r$$



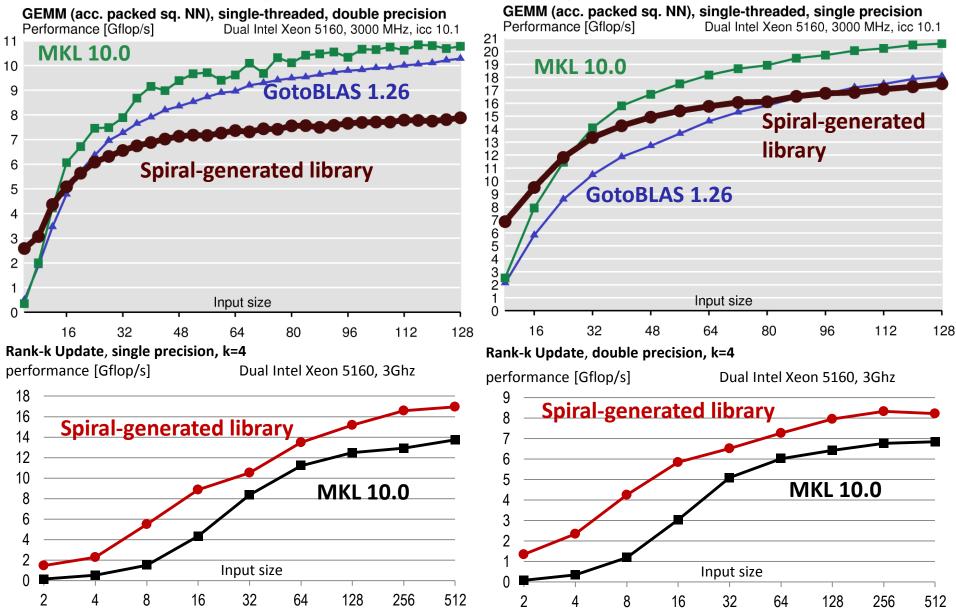
Organization

- Operator language and algorithms
- Optimizing algorithms for platforms
- Performance results
- Summary

SPIRA

www.spiral.net

Matrix Multiplication Library

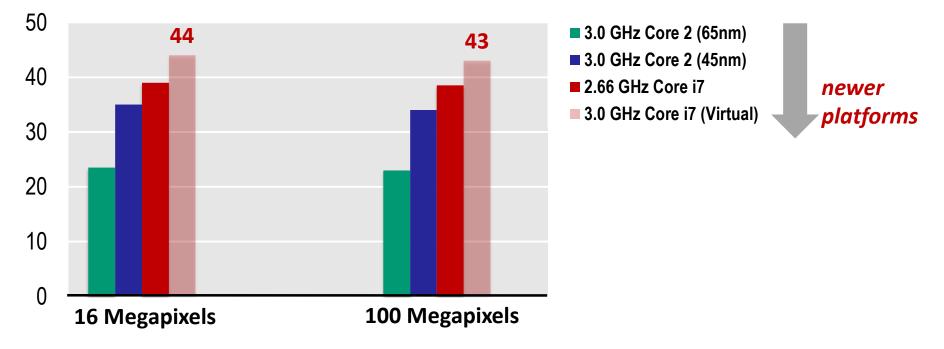




Result: Spiral-Generated PFA SAR on Core2 Quad

SAR Image Formation on Intel platforms

performance [Gflop/s]



- Algorithm by J. Rudin (best paper award, HPEC 2007): 30 Gflop/s on Cell
- Each implementation: vectorized, threaded, cache tuned, ~13 MB of code



Organization

- Operator language and algorithms
- Optimizing algorithms for platforms
- Performance results
- Summary

Summary

 Platforms are powerful yet complicated optimization will stay a hard problem

 OL: unified mathematical framework captures platforms and algorithms

Spiral: program generation and autotuning can provide full automation

Performance of supported kernels is competitive with expert tuning





